

IN THE US PATENT AND TRADEMARK OFFICE

*Steven*  
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2-22-02

Application Number: Not Yet Assigned  
Filing Date: Filed Herewith  
5 Applicant: Glenn Solomon et al  
Agent's Docket No.: CBL-104/DIV  
Application Title: Dual Process Semiconductor  
Heterostructures & Methods  
Examiner: Not Yet Assigned  
10 Art Unit: Not Yet Assigned

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**PRELIMINARY AMENDMENT**

15 Commissioner of Patents and Trademarks  
Washington, DC 20231

Sir:

20 Prior to examination of the above referenced application,  
kindly enter the following:

**TITLE**

25 Kindly amend the title by removing the words "AND METHODS"  
so that the title reads "DUAL PROCESS SEMICONDUCTOR  
HETEROSTRUCTURES."